

In the Specification:

Please replace the paragraph starting on page 14 line 10 with the following amended paragraph:

After receiving the allocated QPN, process 30 prepares one or more work requests (WRs) in the form of descriptors that it writes to memory 28. The descriptors typically specify the type of read or write operation HCA 26 is to perform and the address range in memory 28 from which data are to be read or to which data are to be written. The descriptors are preferably prepared and executed in the form of a linked list, as described in a U.S. patent application entitled "DMA Doorbell," filed May 31, 2001, now US Patent No. 6,735,642, which is assigned to the assignee of the present patent application and whose disclosure is incorporated herein by reference. After the process has prepared the WRs, it causes a service request to be written to its assigned doorbell page 36, at a doorbell ringing step 62. Since multiple QPs can be accessed through the same doorbell page, the service request specifies the QPN, as well as the address in memory 28 at which the first descriptor can be found.